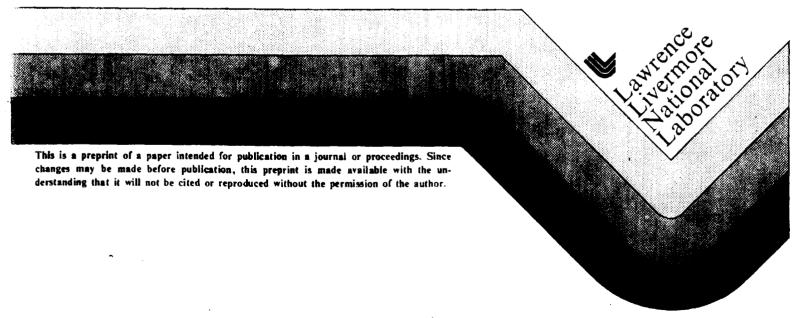
## CARGULATION COPY SUBJECT TO RECALL IN TWO WEEKS

AVALANCHE BREAKDOWN MECHANISMS
AND
APPLICATION TO SWITCHING DEVICES

M. D. Pocha

This paper was prepared for submittal to Workshop on New Directions in Solid State Power Switches
Farmingdale, New York
August 28, 1985

August 20, 1985



#### DISCLAIMER

This document was prepared as an account of work sponsored by an agency of the United States Government. Neither the United States Government nor the University of California nor any of their employees, makes any warranty, express or implied, or assumes any legal liability or responsibility for the accuracy, completeness, or usefulness of any information, apparatus, product, or process disclosed, or represents that its use would not infringe privately owned rights. Reference herein to any specific commercial products, process, or service by trade name, trademark, manufacturer, or otherwise, does not necessarily constitute or imply its endorsement recommendation, or favoring of the United States Government or the University of California. The views and opinions of authors expressed herein do not necessarily state or reflect those of the United States Government or the University of California, and shall not be used for advertising or product endorsement purposes.

# Avalanche Breakdown Mechanisms and Application to Switching Devices

Michael D. Pocha
Lawrence Livermore National Laboratory
P.O. Box 808, Mail Stop L-156
Livermore, California, 94550

August 20, 1985

#### Introduction:

The purpose of this report is threefold. 1) to present a discussion of avalanche breakdown which is the primary high voltage limiting mechanism in semiconductor junction devices. 2) to discuss surface field effects on avalanche breakdown and junction termination techniques for reducing these surface effects. 3) to present a discussion of avalanche second breakdown and its application to high voltage, high speed, switching devices. One of the goals of these discussions is to present the current state-of-the-art in the area of junction breakdown and high voltage junction devices. Since only a cursory description of the subjects can be given in this short report, a large list of references is compiled to help the reader pursue these areas further.

#### Avalanche Breakdown:

Junction breakdown in high voltage semiconductor devices is almost always determined by the avalanche breakdown process. Avalanche breakdown occurs when a carrier moving in the reverse bias electric field of the depletion layer can gain sufficient energy to knock another carrier out of the conduction or valence band during a collision. This process is strongly dependent on the electric field in the depletion layer. The probability of breakdown for a given electric field distribution is calculated by the ionization integrals which integrate the ionization coefficients across the depletion layer [1-2]. Fig. 1 shows the ionization coefficients for electrons and holes as functions of electric field and temperature. The curves for the higher temperatures are extrapolated from the lower temperature data and may not be entirely accurate. Because of the strong electric field dependence of breakdown, all techniques for increasing breakdown voltage involve the reduction of electric field in the depletion layer. In the one dimension into the bulk the task is relatively simple very high breakdown voltage can theoretically be achieved by simply making the

p and n regions lightly doped and very wide. The real limiting effects are the two and three dimensional electric field effects at the edge of the semiconductor device where the junction comes to the surface.

#### Surface Breakdown:

Surface breakdown, as referred to here, is the effect of a reduced p-n junction breakdown because of a higher electric field across the junction at the surface of a semiconductor device [3]. The causes of this increased surface electric field are many, including dangling bonds at the surface of the semiconductor material, differences in dielectric constant between the semiconductor and the material surrounding it, fixed charge in the material coating the semiconductor surface, and curvature of diffused junctions. A variety of techniques have been suggested to reduce the surface electric field and thereby increase breakdown voltage [4]. These include beveling and etching the surface to change the charge distribution near the junction edge [5-9], field limiting diffusions [10], and field plates covering the junction edge [11-13]. Among these reports, the highest breakdown voltage was 10 KV, achieved using a semi-insulating polysilicon (SIPOS) field plate [13].

### Second Breakdown and Switching:

If the current through a device is allowed to increase after breakdown is reached, a second breakdown is seen wherein the voltage across the device collapses to a much lower value as shown in Fig. 2. This negative resistance region of the characteristic is very useful for very high speed switching. A switching device called the "Avalanche Transistor" is based on this phenomenon [14]. Switches capable of holding off up to 1000 V and conducting several 10s of amps of current have been made with switching times of less then 5 nanoseconds [15]. This second breakdown phenomenon is not very well understood. Recently, two distinct regimes of second breakdown were described [16], one called thermal mode is relatively slow (microseconds), the other called current mode is fast (nanoseconds). One explanation of second breakdown is that it is a feed back effect in which carriers generated by avalanche multiplication forward bias the emitter base junction of bipolar transistors [17]. The discovery of a similar second breakdown in diodes tends to shed some doubt on this theory [18-19]. In fact the switching speed in diodes is substantially faster than in transistors. Fig. 3 is a pulse generated, by a circuit using one of these diodes, in our laboratory having a peak amplitude of 2800 volts (56 Amps.) and a rise time of less than 0.5 ns. These devices are

extremely useful for generation of high voltage pulses where low jitter and electrical triggering are a requirement.

#### References:

- 1. R. van Overstraeten and H. de Man, "Measurement of the ionization rates in diffused silicon p-n junctions," <u>Solid-State electronics</u>, Vol. <u>13</u>, P. 583, 1970.
- 2. W. N. Grant, "Electron and Hole Ionization Rates in Epitaxial Silicon at High Electric Fields," <u>Solid State Electronics</u>, Vol. <u>16</u>, P. 1189, 1973.
- 3. A. S. Grove, <u>Physics and Technology of Semiconductor Devices</u>, Sec. 10.5, P. 311, John Wiley and Sons, New York 1967.
- 4. Adolph Blicher, <u>Field-Effect and Bipolar Power Transistor Physics</u>, Chapter 4, P. 52, Academic Press, New York, 1981.
- 5. R. L. Davies, F. E. Gentry, "Control of Electric Field at the Surface of P-N Junctions," <u>IEEE Transactions on Electron Devices</u>, <u>ED-11</u>, P. 313, 1964.
- 6. V. A. K. Temple, M.S. Adler, "The Theory and Application of a Simple Etch Contour for Near Ideal Breakdown Voltage in Plane and Planar p-n Junctions," <u>IEEE Transactions on Electron Devices</u>, <u>ED-23</u>, P. 950, 1976
- 7. V.A.K. Temple, M.S. Adler, "Calculations of the Diffusion Curvature Related Avalanche Breakdown in High-Voltage Planar p-n-Junctions," <u>IEEE Transactions on Electron Devices</u>, <u>ED-22</u>, P. 910, 1975.
- 8. V.A.K. Temple, "Increased Avalanche Breakdown Voltage and Controlled Surface Electric Fields Using a Junction Termination Extension (JTE) Technique," <u>IEEE Transactions on Electron Devices</u>, <u>ED-30</u>, P. 954, 1983.
- 9. K.P. Brieger, W. Gerlach, J. Pelka, "The Influence of Surface Charge and Bevel Angle on the Blocking Behavior of a High-Voltage P<sup>+</sup>-n-n<sup>+</sup> Device," <u>IEEE Transactions on Electron Devices</u>, <u>ED-31</u>, P. 733, 1984.
- 10. M.S. Adler, V.A.K. Temple, A.P. Ferro, R.C. Rustay, "Theory and Breakdown Voltage for Planar Devices with a Single Field Limiting Ring," <u>IEEE</u>
  <u>Transactions on Electron Devices</u>, <u>ED-24</u>, P. 107, 1977.

- 11. F. Conti, M. Conti, "Surface Breakdown in Silicon Planar Diodes Equipped with Field Plate," <u>Solid State Electronics</u>, Vol. <u>15</u>, P. 93, 1971.
- 12. F.A. Selim, "High-Voltage, Large-Area Planar Devices," <u>IEEE Electron Device Letters</u>, <u>EDL-2</u>, P. 219, 1981.
- 13. T. Matsushita, T. Aoki, T. Ohtsu, H. Yamoto, H. Hayashi, M. Okayama, Y. Kawana, "Highly Reliable High-Voltage Transistors by use of the SIPOS Process," IEEE Transactions on Electron Devices, ED-23, P. 826, 1976.
- 14. D.J. Hamilton, J.F. Gibbons, W.Shockley, "Physical Principles of Avalanche Transistor Pulse Circuits," <u>Proceedings of the IRE</u>, P. 1102, June 1959.
- 15. J. C. Koo, M. D. Pocha, "High Voltage, High Speed Switching Transistors Using Current Mode Second Breakdown," <u>Proceedings</u>, 33rd Electronic Components Conference, IEEE Catalog No. 83CH1904-2, P. 178,1983
- 16. K. Koyanagi, K. Hane, T. Suzuki, "Boundary Conditions Between Current Mode and Thermal Mode Second Breakdown in Epitaxial Planar Transistors," <u>IEEE Transactions on Electron Devices</u>, <u>ED-24</u>, P. 672, 1977.
- 17. J.E. Carroll, P.J. Probert, "Current/voltage characteristics of transistors operating in current-mode second breakdown," <u>Solid-State and Electron</u> <u>Devices</u>, Vol. <u>3</u>, P. 41, 1979.
- 18. I.V. Grekhov, A.F. Kardo-Sysoev, L.S.Kostina, S.V. Shenderey, A.F. Ioffe, "High Power Subnanosecond Switch," <u>Technical Digest</u>, 1980 International Electron Devices Meeting, Washington D.C., P. 662.
- 19. I.V.Grekhov, A.F. Kardo-Sysoev, L.S. Kostina, "Breakdown delay and excitation of ionization waves in p-n junctions," <u>Soviet Tech. Phys. Letters</u>, Vol. <u>5</u>, P. 399, Aug. 1979.

This work was performed under the auspices of the U.S. Department of Energy by Lawrence Livermore National Laboratory under contract No. W-7405-Eng-48.

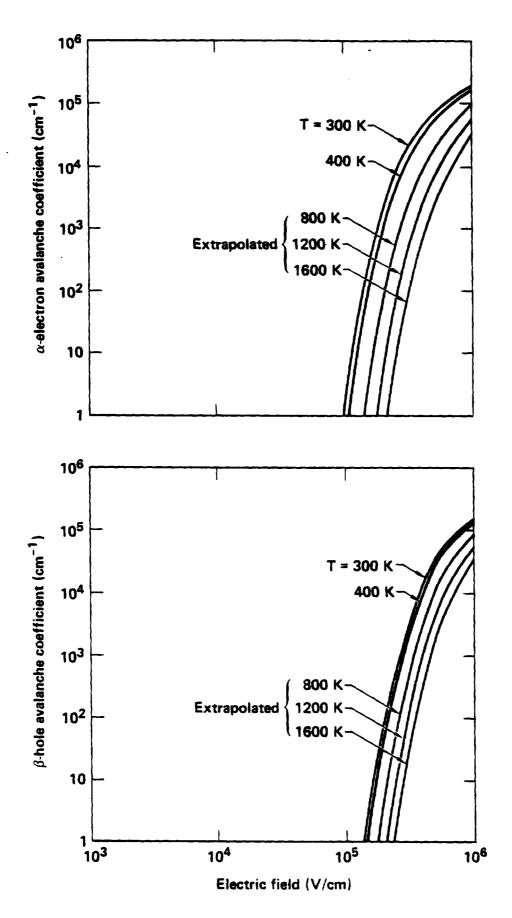


Figure 1. Ionization coefficients verses electric field for several temperatures.

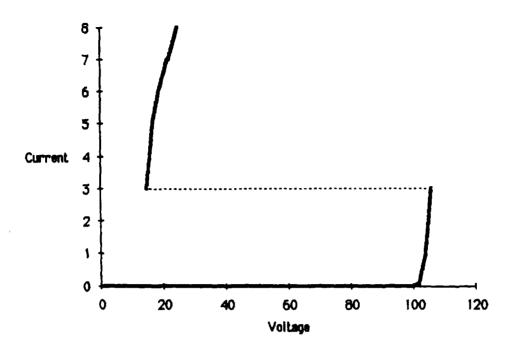


Figure 2. Current/voltage characteristics illustrating second breakdown in a junction semiconductor device

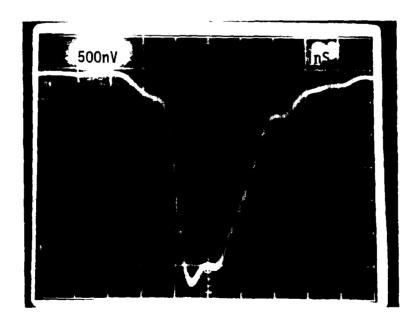


Figure 3. A 2800 volt 300 picosecond rise time pulse generated using the fast second breakdown effect in a silicon diode.